

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Dale John Shidla, et al.

Confirmation No.:

Application No.: 10/659,079

Examiner: unknown

Filing Date: Sept. 10, 2003

Group Art Unit:

Title: OPPORTUNISTIC CPU FUNCTIONAL TESTING WITH HARDWARE COMPARE

COMMISSIONER FOR PATENTS

Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ( ) under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- (X) under 37 CFR 1.97 (c) together with either a:  
(X) Statement under 37 CFR 1.97(e), or  
( ) a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ( ) under 37 CFR 1.97 (d) together with a:  
( ) Statement under 37 CFR 1.97(e), and  
( ) a petition under 37 CFR 1.97(d)(2), and  
( ) a \$180.00 petition fee set forth in 37 CFR 1.17(p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

(X) Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

( ) A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: Jan. 25, 2005

Typed Name: James K. Okamoto

Signature: James K. Okamoto

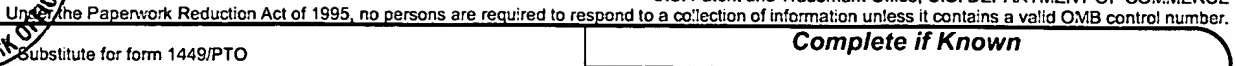
Respectfully submitted,

Dale John Shidla, et al.

By James K. Okamoto  
James K. Okamoto

Attorney/Agent for Applicant(s)  
Reg. No. 40,110

Date: Jan. 25, 2005



# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet	1	of	2
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**Complete if Known**

Application Number	10/659,079
Filing Date	September 10, 2003
First Named Inventor	Dale John Shidla
Art Unit	
Examiner Name	unknown
Attorney Docket Number	200310484-1

## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
	1J	EP 0 260 584 A2	03-23-1988	Honeywell Bull		
	1K	GB 1 409 466 A	10-08-1975	Siemens		

Examiner Signature		Date Considered	
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<sup>1</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>2</sup>Applicant's unique citation designation number (optional). <sup>3</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>4</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>5</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>6</sup>Kind of document by the appropriate symbol as indicated on the document under WIPO Standard ST.16 if possible. <sup>7</sup>Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

**Complete if Known**

Application Number 10/659,079

Filing Date September 10, 2003

First Named Inventor Dale John Shidla

Art Unit

Examiner Name unknown

Sheet

2

of

2

Attorney Docket Number 200310484-1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1L	T.H. CHEN, et al. "Concurrent error detection in array multipliers by BIDO" Nov. 1995, pgs. 425-430, Vol. 142, No. 6, IEE Proc.-Comput. Digit. Tech.	
	1M	W. HAHN, et al. "Psuedoduplication of Floating-Point Addition-A Method of Compiler Generated Checking of Permanent Hardware Faults" 1991, pgs. 161-165, IEEE VLSI Test Sympo.	
	1N	Patents Act 1977: Search Report under Section 17 for Application No. GB 0 418 639.1	

Examiner  
SignatureDate  
Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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